



Docket No.: 826.1707

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Makoto NAKANISHI

Serial No. 09/811,484

Group Art Unit: 2185

Confirmation No. 7934

Filed: March 20, 2001

Examiner: Unassigned

For: MATRIX PROCESSING METHOD OF SHARED-MEMORY SCALAR PARALLEL-
PROCESSING COMPUTER AND RECORDING MEDIUM

RULE 1.312 AMENDMENT

Mail Stop Issue Fee

Commissioner for Patents

PO Box 1450

Alexandria, VA 22313-1450

Sir:

Applicant(s) received a Notice of Allowance dated December 1, 2004 in connection with the above-identified application. Please amend the above-identified application as follows, in accordance with the provisions of 37 CFR § 1.312 and for the reasons discussed in the accompanying Remarks.